

Day 1 - Program description

- 09:00 - 09:30** Open doors and registration [Coffee and croissants]
- 09:30 - 10:15** Key note speech [H. Schmid]: **"Electrical and Human Feedback"**
- 10:15 - 11:00** Presentation [R. Grandi]: **"Virtual trip around the IC supply chain"**
- 11:00 - 11:30** Coffee break [Coffee and beverages, networking]
- 11:30 - 12:15** Presentation [G. Keel]: **"Why ASIC Designs are different"**
- 12:15 - 14:00** Lunch break [Business lunch with sandwiches, networking at Lichthof]
- 14:00 - 14:45** Presentation [P. Kaiser]: **"CAD Methods to Improve Yield"**
- 14:45 - 15:15** Coffee break [Coffee and beverages, networking]
- 15:15 - 16:00** Presentation [L. v.d.Logt]: **"Model driven test development and simulation"**
- 16:00 - 16:45** Presentation [P. Salome]: **"ESD Challenges for fabless companies"**
- 17:00 - 18:30** Cultural event [Museum visit, networking]
- 19:15 - 22:00** Social event [Dinner, networking]

Day 2 - Program description

09:00 - 09:30 Open doors and registration [Coffee and croissants]

09:30 - 12:00 **Hands-on Tanner EDA tool - Part I**
- Schematic Entry
- Simulation

12:00 - 13:30 Lunch break [Business lunch with sandwiches]

13:30 - 16:00 **Hands-on Tanner EDA tool - Part II**
- IC Layout
- DRC, LVS

For detailed information and registration please go to www.ms-asics.ch.

In collaboration with